Fig. 1

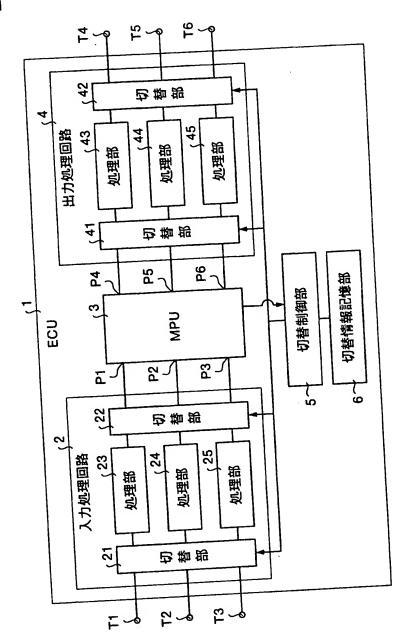


Fig. 2

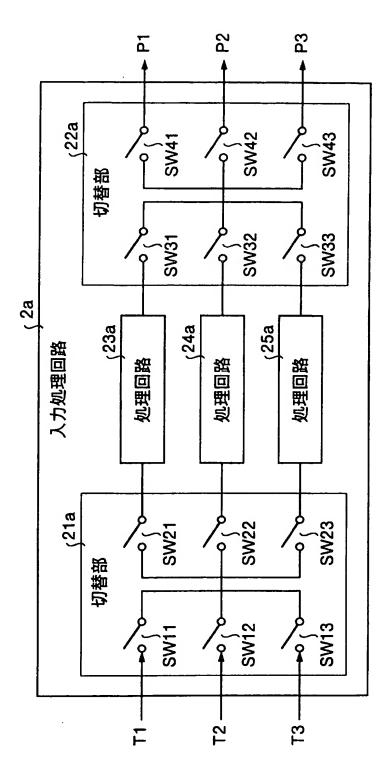
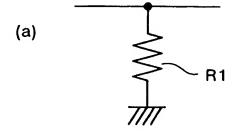
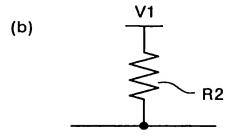
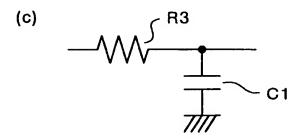


Fig. 3







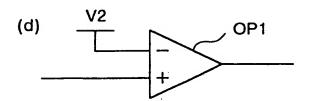


Fig. 4

(a)

入力端子T1	処理A	入力ポートP2
入力端子T2	処理B	入力ポートP3
入力端子T3	処理C	入力ポートP1

(b)

入力端子T1	スイッチS11	スイッチS21	スイッチS31	スイッチS42
入力端子T2	スイッチS12	スイッチS22	スイッチS32	スイッチS43
入力端子T3	スイッチS13	スイッチS23	スイッチS33	スイッチS41

Fig. 5

(a)

入力端子T1	処理B	入力ポートP2
入力端子T2	処理A	入力ポートP3
入力端子T3	処理C	入力ポートP1

(b)

入力端子T1	スイッチS11	スイッチS22	スイッチS32	スイッチS42
入力端子T2	スイッチS12	スイッチS21	スイッチS31	スイッチS43
入力端子T3	スイッチS13	スイッチS23	スイッチS33	スイッチS41

Fig. 6

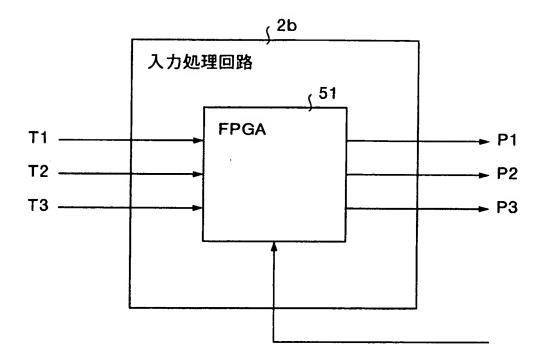


Fig. 7

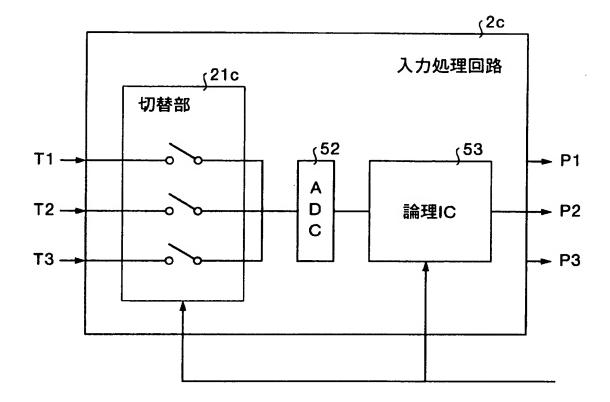


Fig. 8

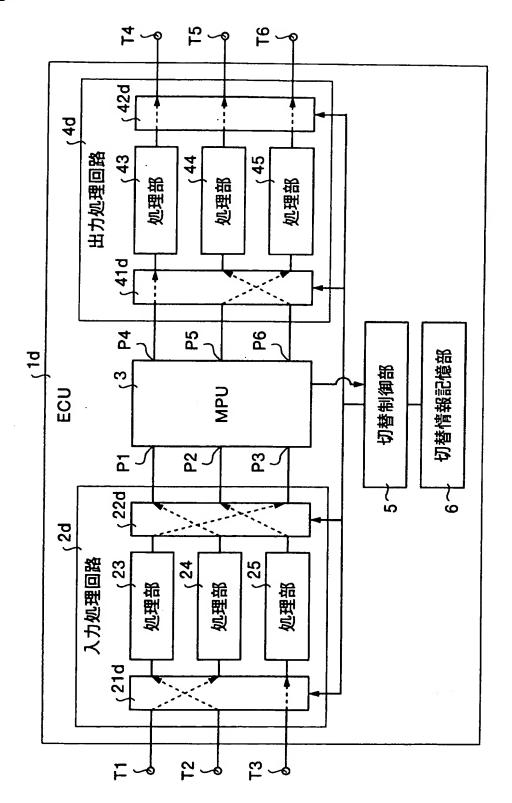


Fig. 9

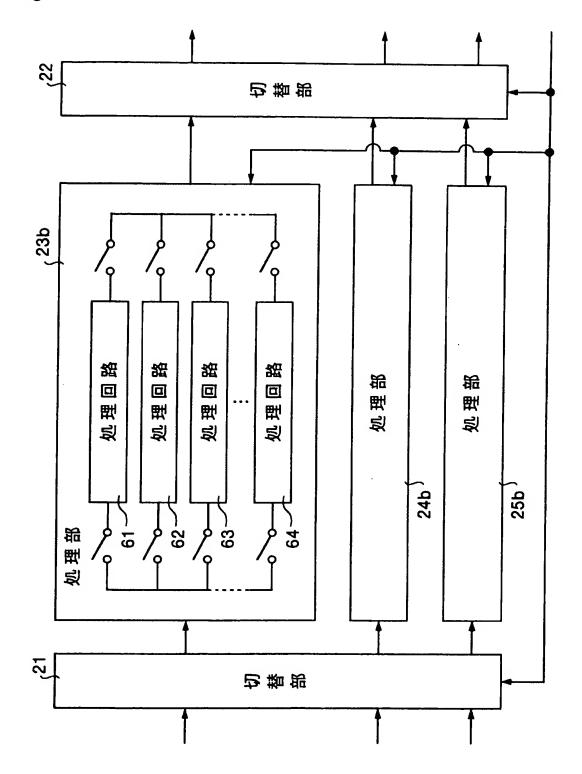


Fig. 10

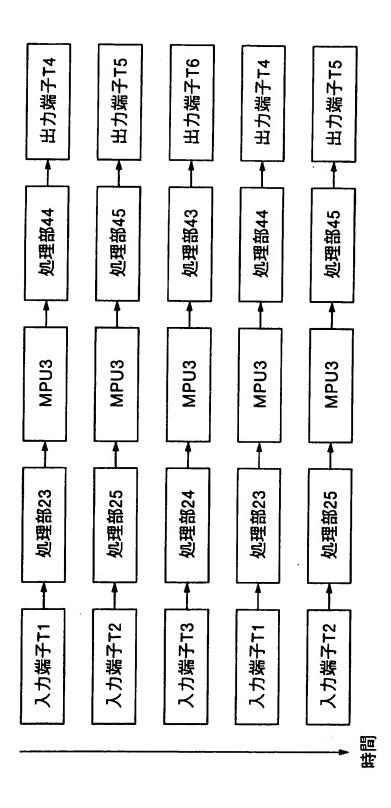


Fig. 11

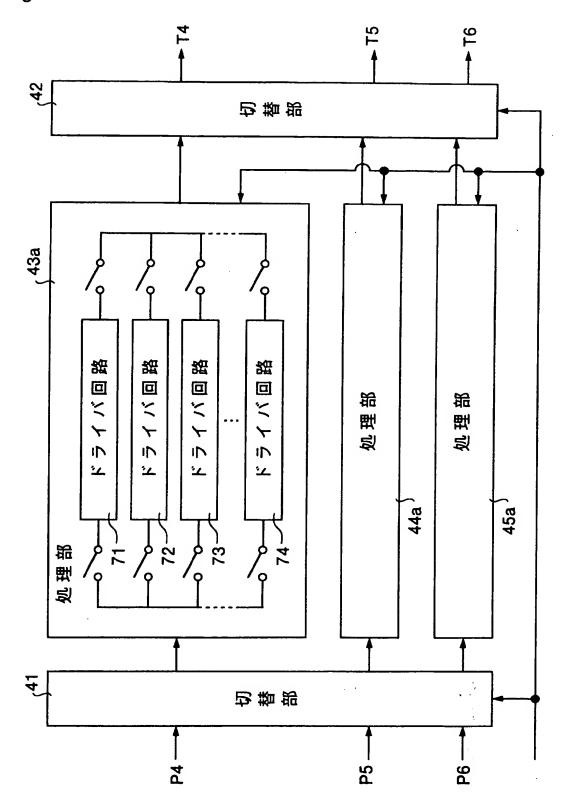


Fig. 12

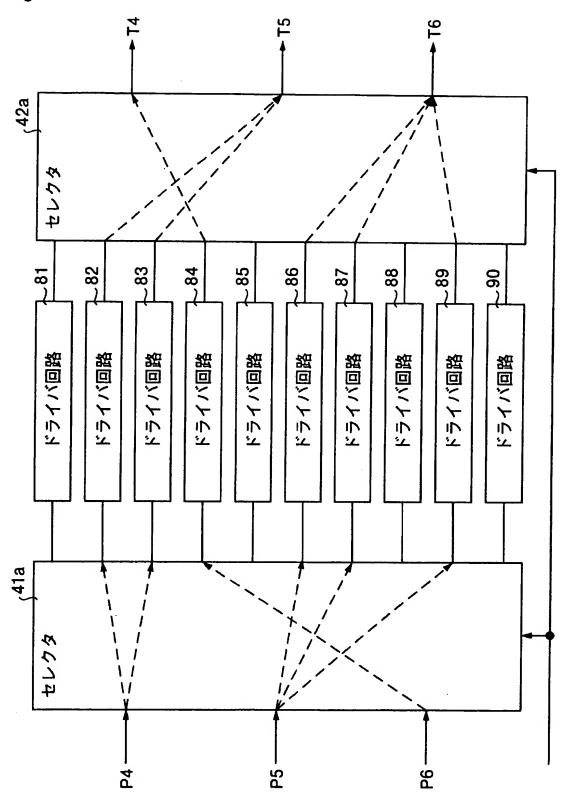


Fig. 13

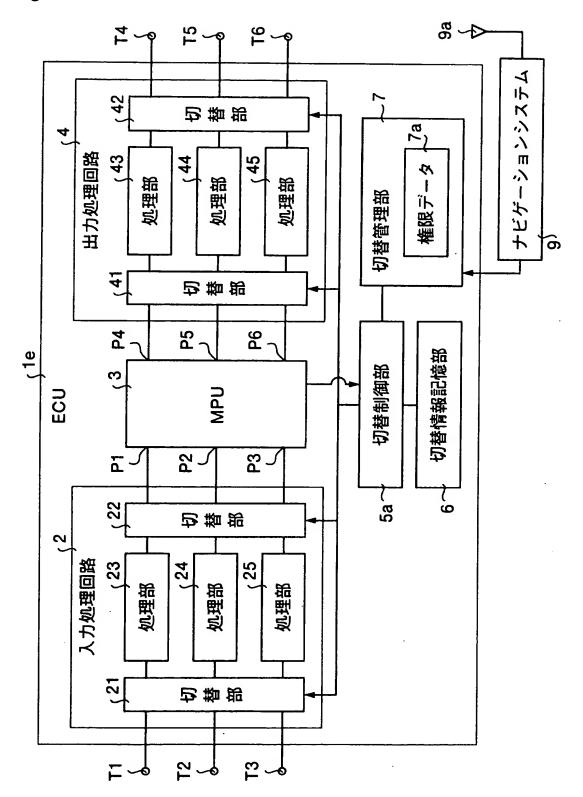


Fig. 14

∫ 7a

	処理A	処理B	処理C	処理D	
権限レベル2	可	可	可	可	
権限レベル1	可	不可	不可	可	
権限レベル0	不可	不可	不可	不可	

Fig. 15

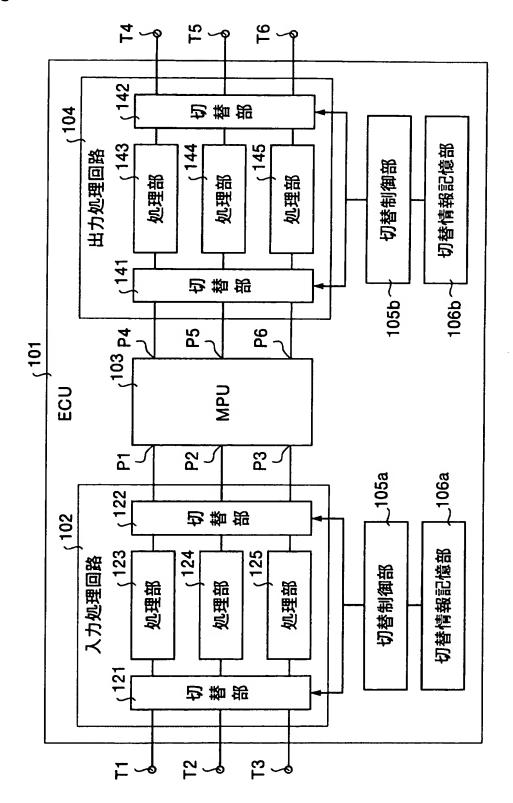


Fig. 16

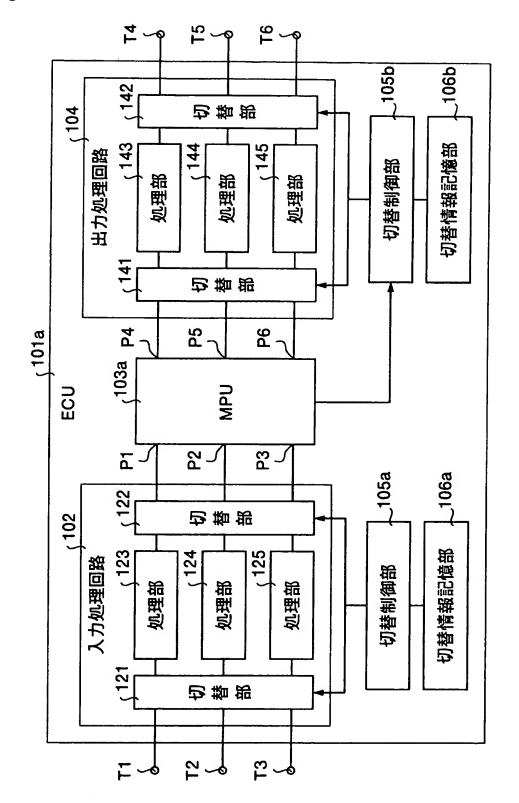


Fig. 17

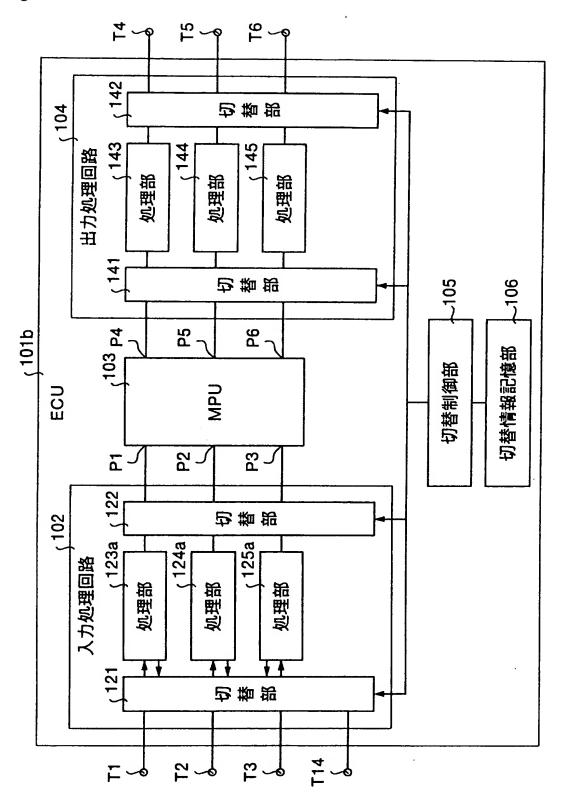
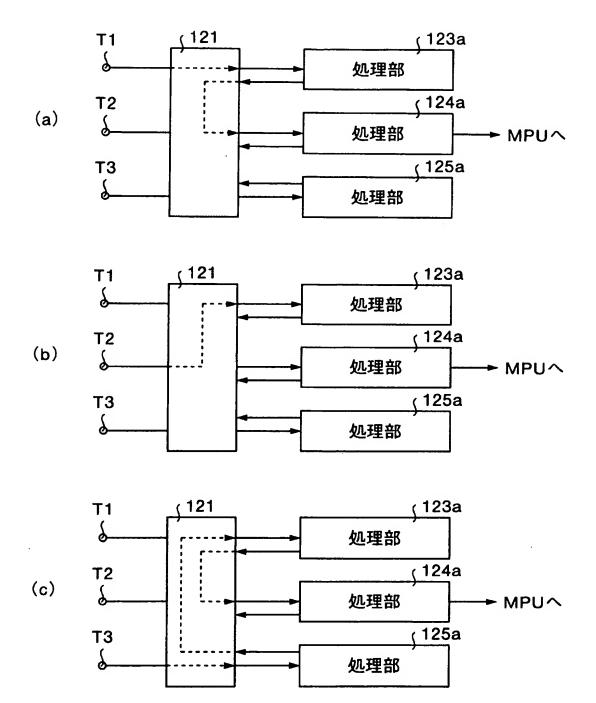


Fig. 18



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Fig. 19

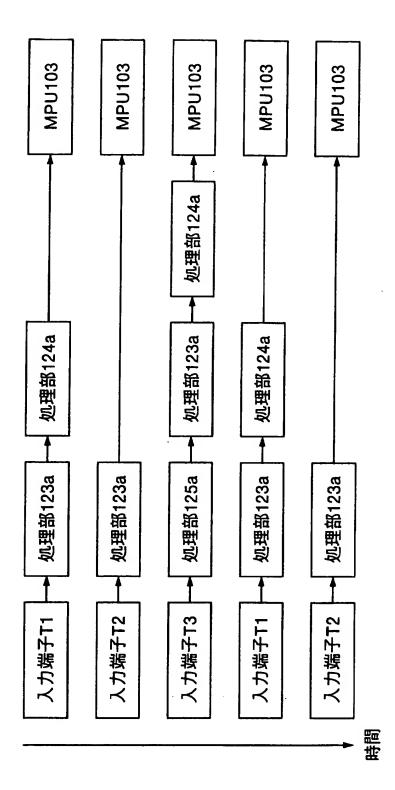


Fig. 20

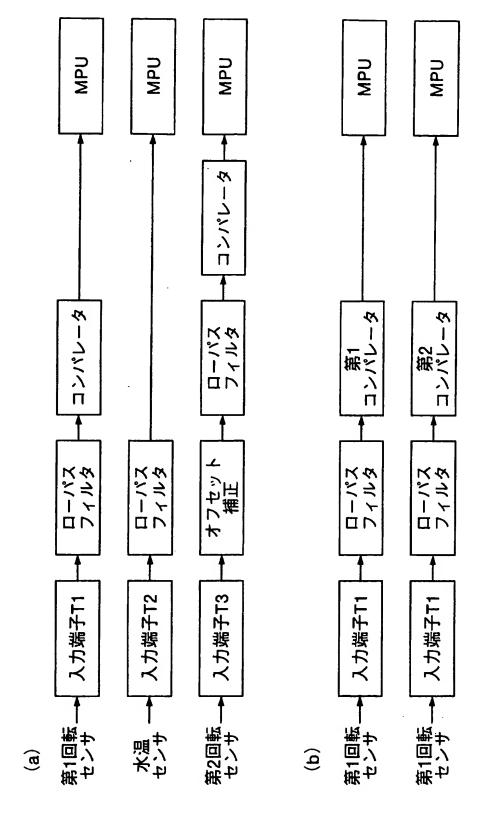


Fig. 21

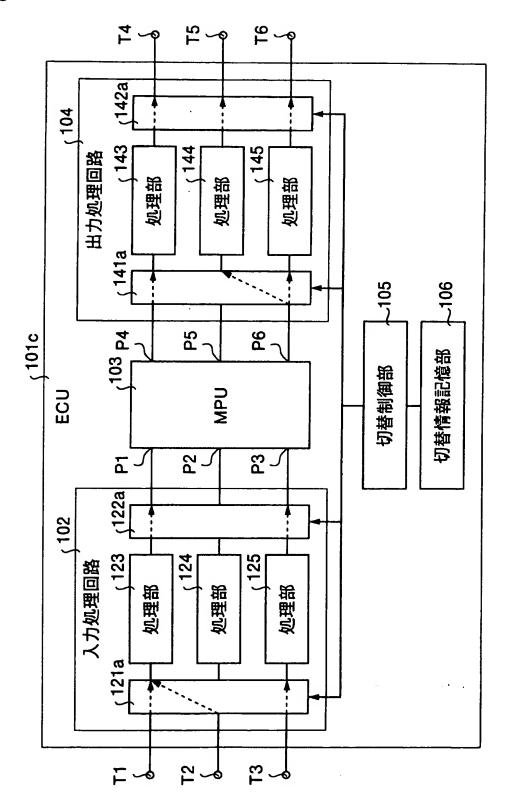


Fig. 22

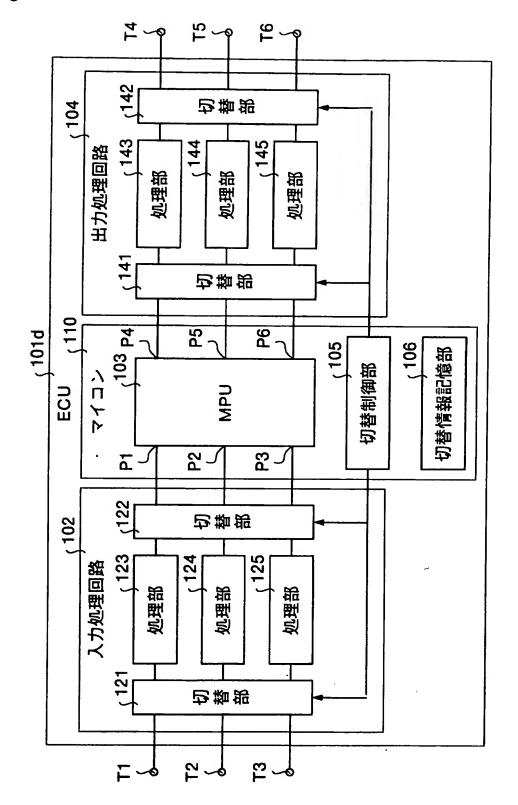


Fig. 23

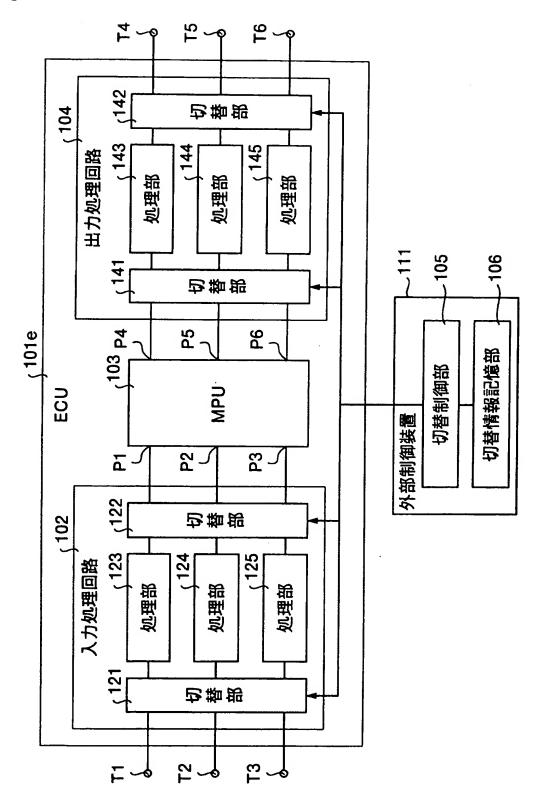


Fig. 24

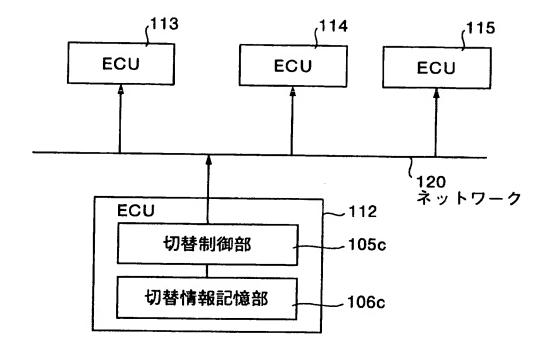


Fig. 25

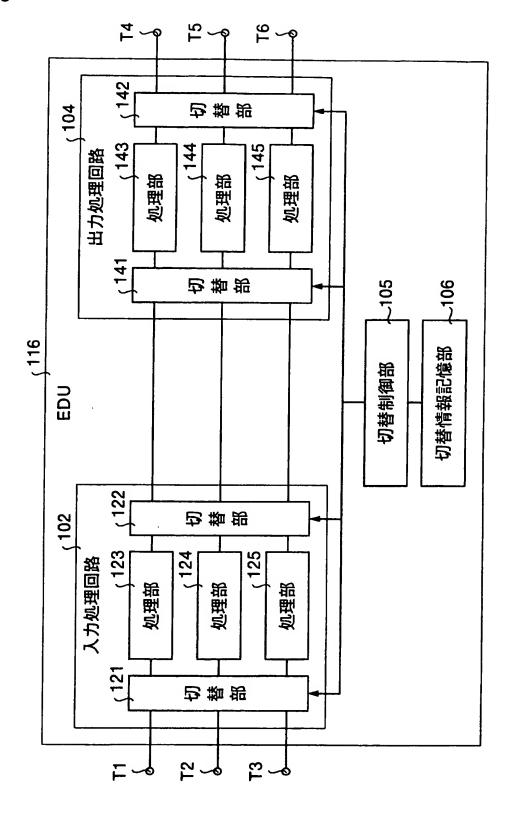


Fig. 26

